

Figure 1

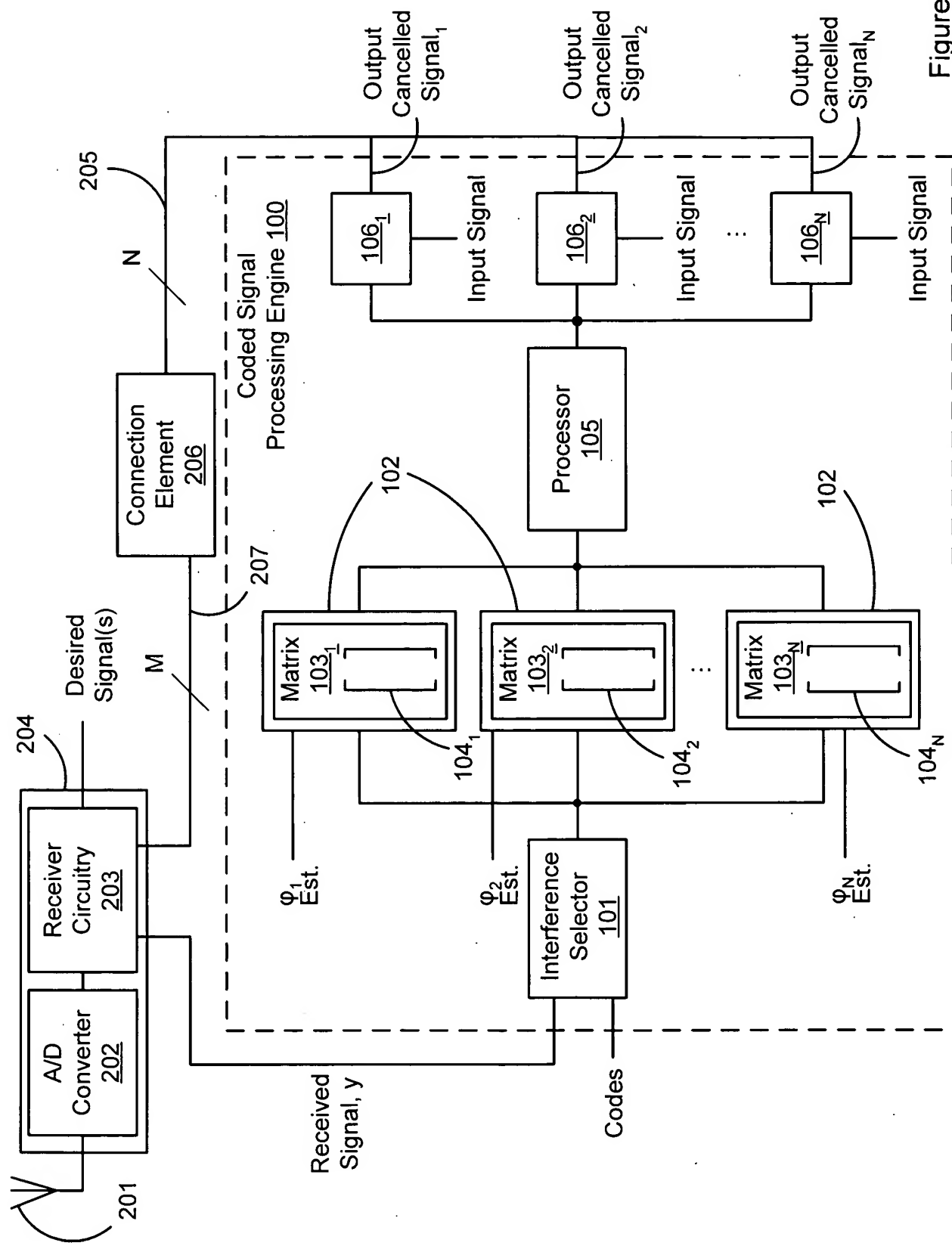


Figure 2

# Receiver Circuitry 203

for a  $P_s^\perp$  y Operation

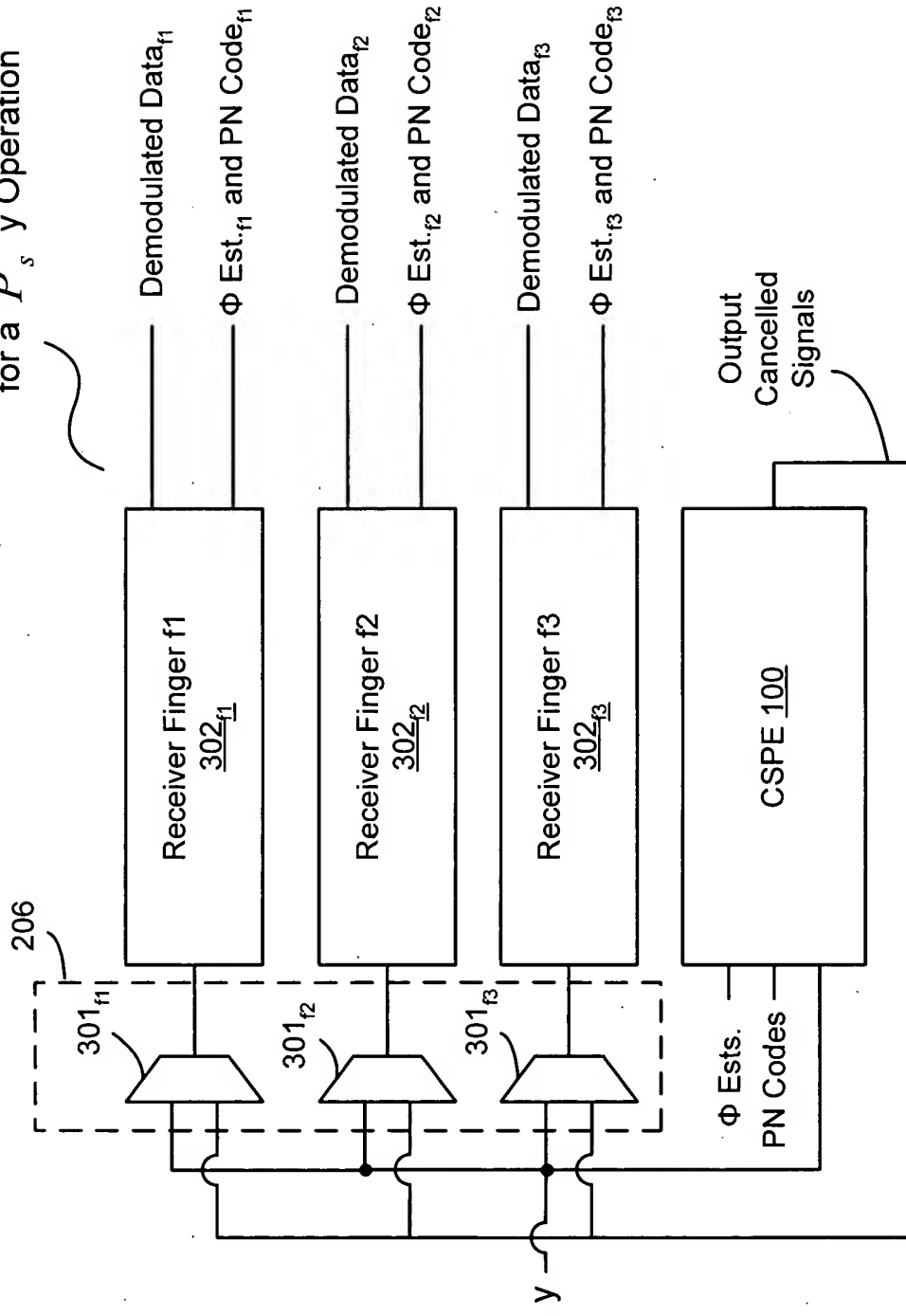


Figure 3

# Receiver Circuitry 203

for a  $P_s^\perp \times$  Operation

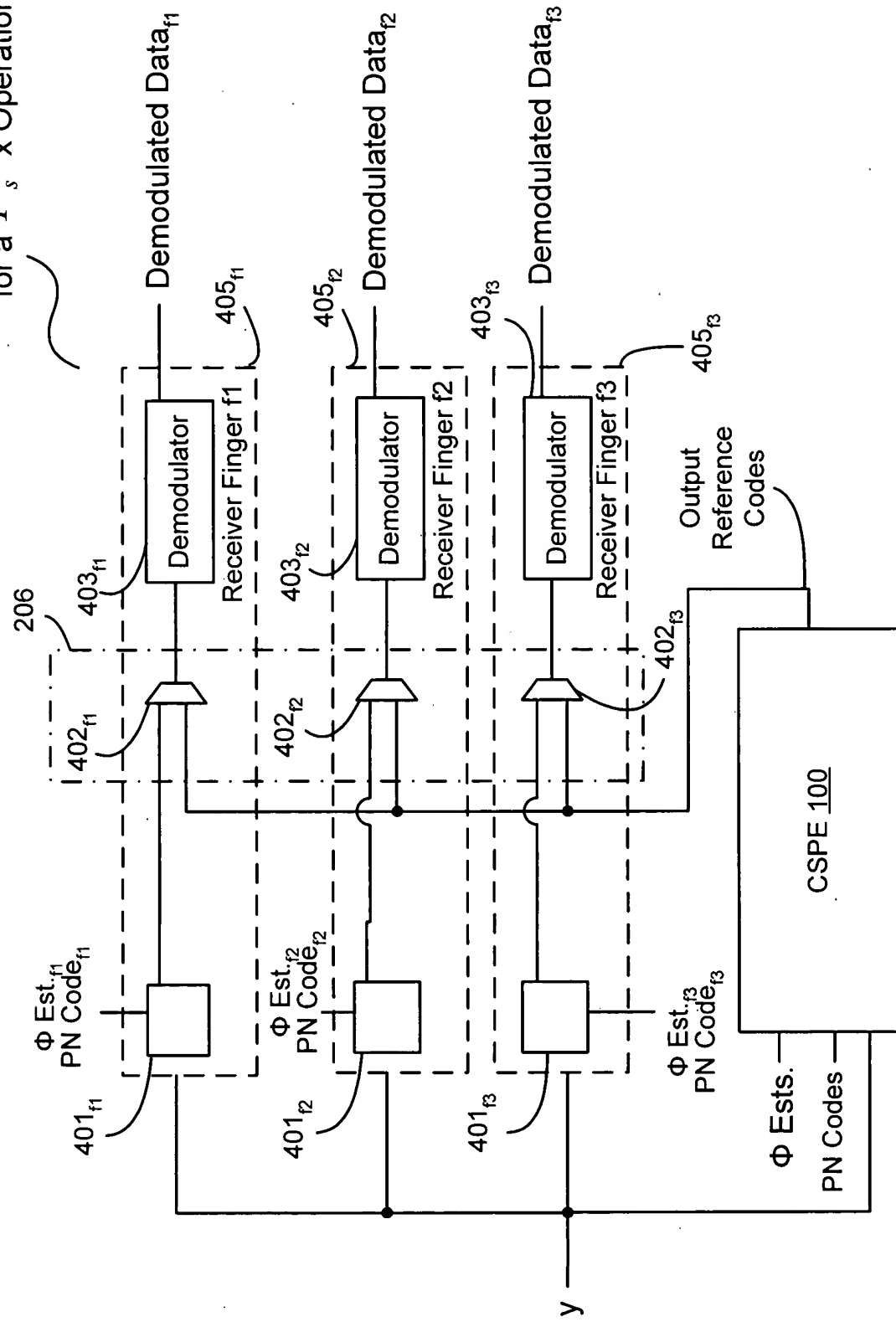


Figure 4

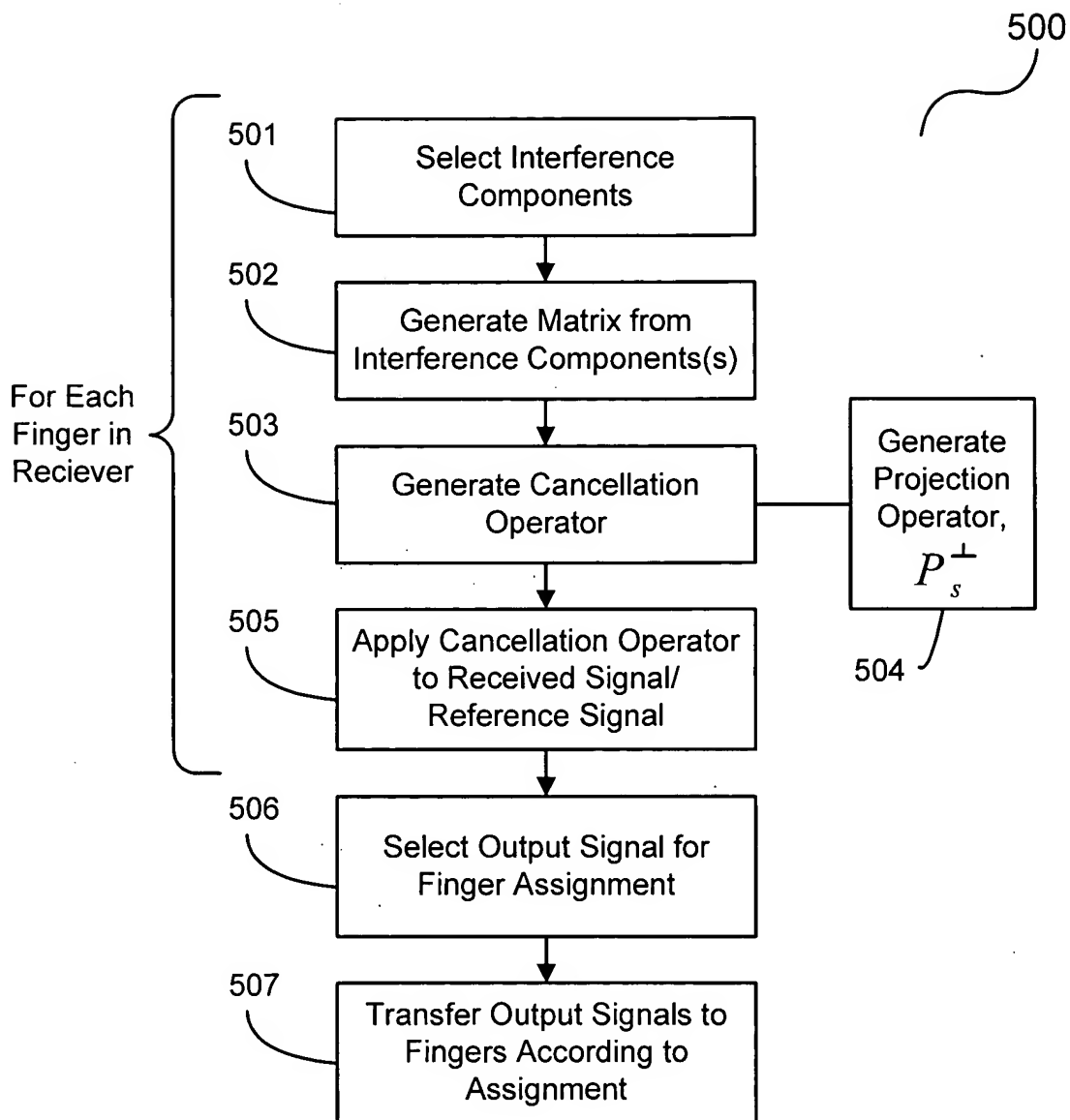


Figure 5